

EAST - [Untitled1:1]

File View Edit Tools Window Help

Drafts

Pending

Active

L1: (156) ("low pin cou

L2: (8) l1 and (firmwar

Failed

Saved

Favorites

Tagged (0)

UDC

Queue

Trash

Search

Let

Browse

Queue

Clear

DBs

USPAT

Default operator

OR

Plurals

Highlight all hit terms initially

BRS form

IS&R form

Image

Text

HTML

	Type	L #	Hits	Search Text	DBs	Time Stam	Comment	Error	Definit	Er
1	BRS	L1	156	("low pin count" or LPC) near5 memory	USPA T	2005/09/2 2 16:58				
2	BRS	L2	8	l1 and (firmware near5 memory)	USPA T	2005/09/2 2 16:59				

Start

EAST - [...]

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☐ Drafts
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 L1: (156) ("low pin cou
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Search
 DBs: USPAT ☒ Plurals ☒ Highlight all hit terms initially
 Default operator: OR

l1 and (firmware near5 memory)

☒ BRS form ☒ IS&R form ☐ Image ☐ Text ☐ HTML

	U	I	Document ID	Issue Dat	Pages	Title	Current OR	Current XR
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6927991 B2	20050809	9	Memory device accessible with differe	365/63	365/189.07
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6904484 B1	20050607	7	Low pin count (LPC) firmware hub recovery	710/300	365/185.08
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6851014 B2	20050201	13	Memory device having automatic protocol dete	711/103	711/145; 713/2
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6421765 B1	20020716	9	Method and apparatus for selecting functiona	711/145	711/100; 711/154;
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6381693 B2	20020430	10	Arrangements having firmware support for di	713/1	713/2
6	<input type="checkbox"/>	<input type="checkbox"/>	US 5835965 A	19981110	23	Memory system with multiplexed input-outpu	711/211	711/202
7	<input type="checkbox"/>	<input type="checkbox"/>	US 5829016 A	19981027	11	Memory system with multiplexed input-outpu	711/111	365/189.02
8	<input type="checkbox"/>	<input type="checkbox"/>	US 5091950 A	19920225	31	Arabic language translating device with	704/277	704/3; 704/7